

Fig. 1

<b></b>		
1.1		
	O MIKE  1 2 3 4 5 6 7 8 9 0  RAND RPT SCAN R-M EQ < > STILL	

Fig.2



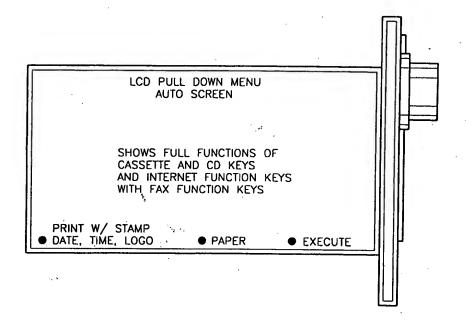


Fig.3

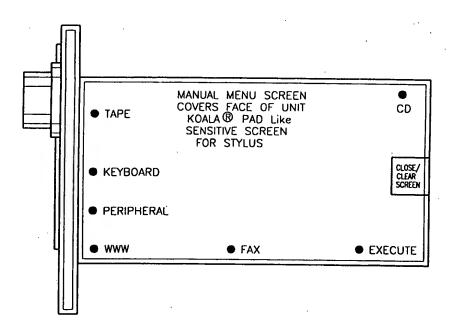


Fig.4



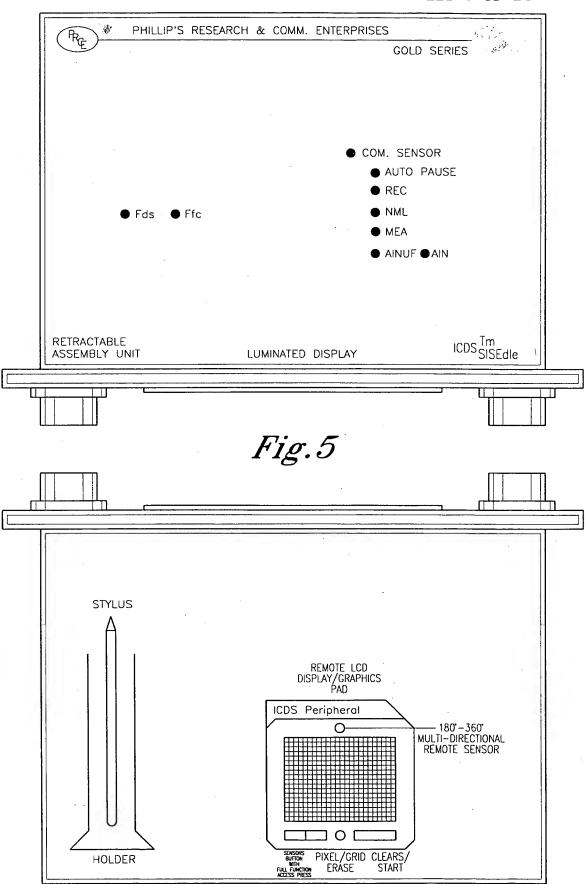
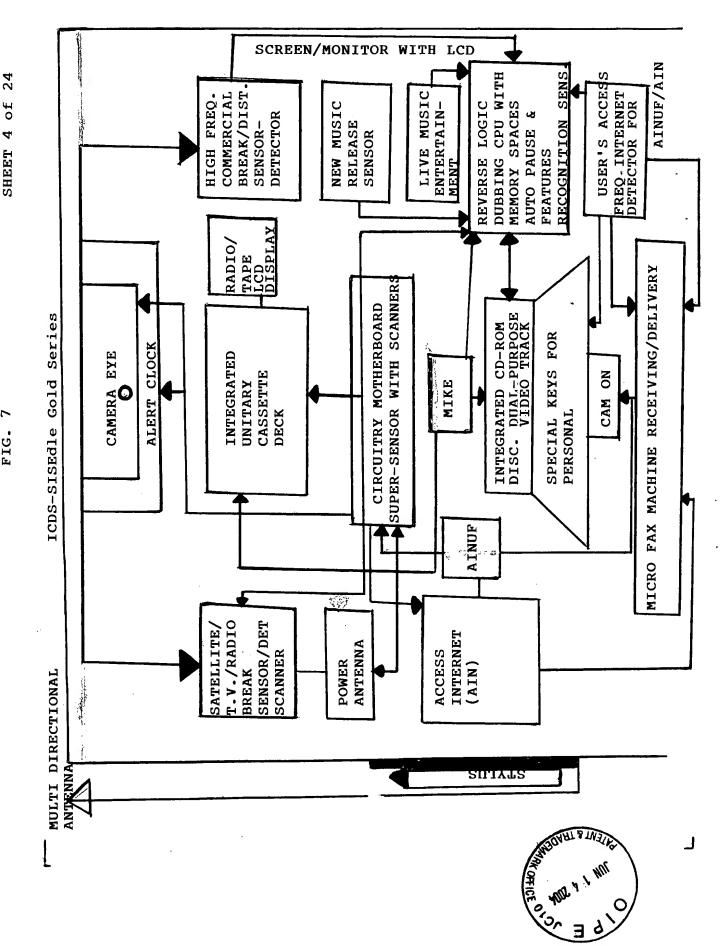


Fig.6

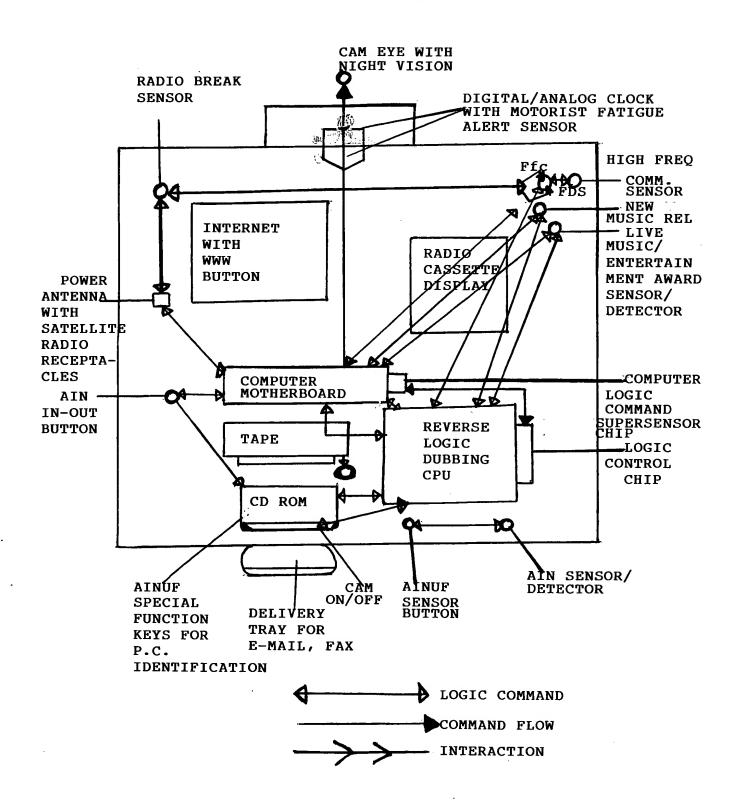
7

FIG.

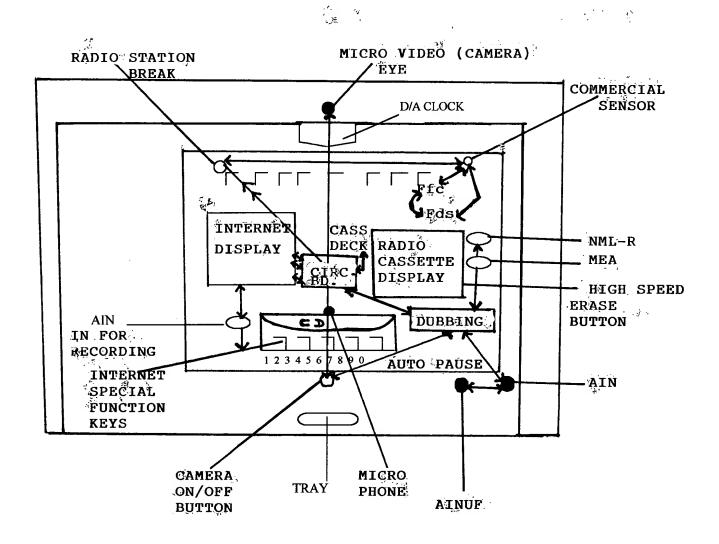


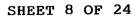


### BLUE PRINT











# FIG. 11

ICDS-SISE GOLD SERIES - Top of the Model

MÓDE:

CSISX MODEL

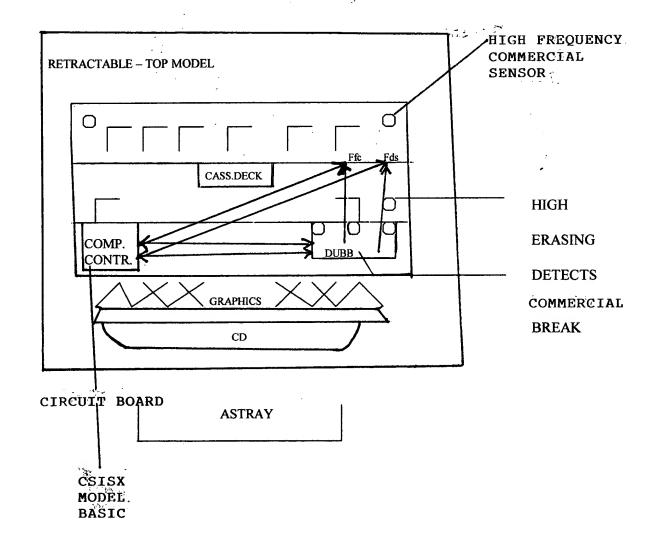
- Commercial Sensor deactivated

SIS MODEL

- Commercial Super Sensor Chip

SISE MODEL

- Erasing Key (deluxe)

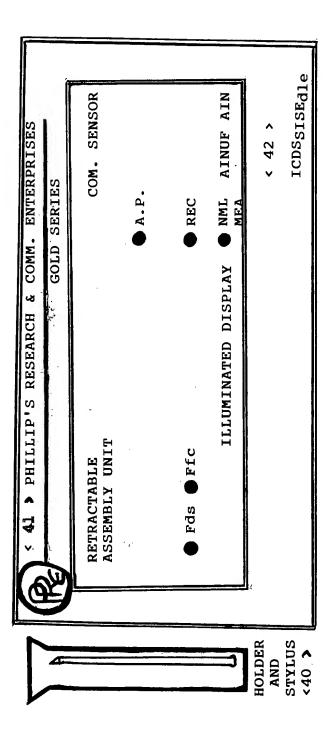


**A**.

FIG. 12

SHEET 9 OF 24

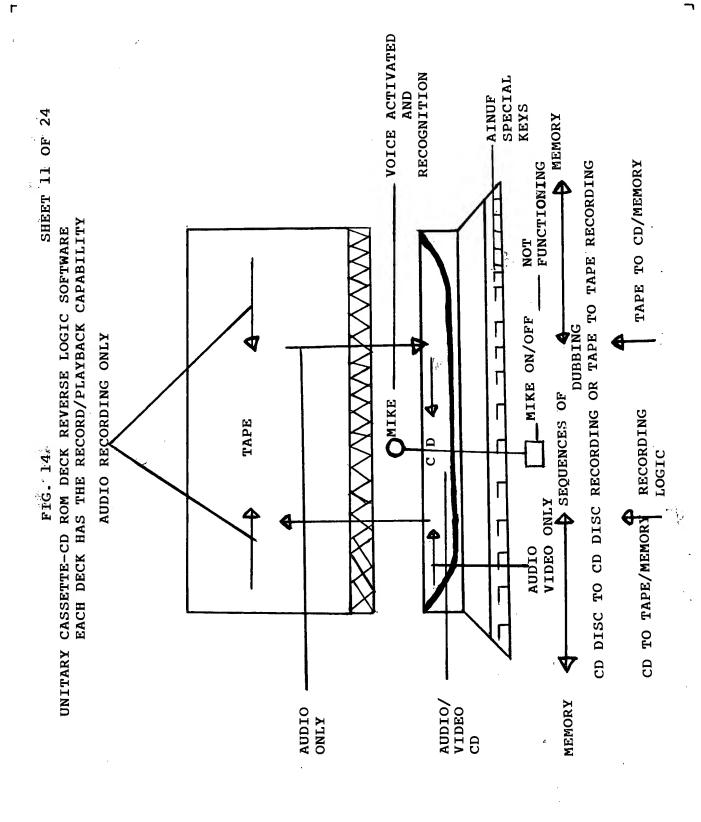
18351 U.S. PTO





١

T



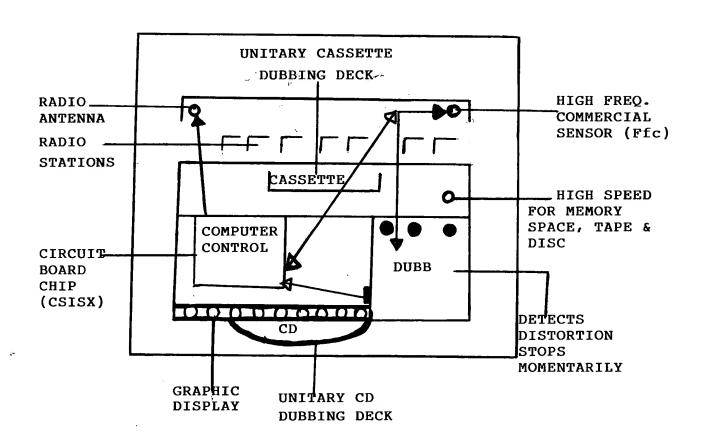
L

:4

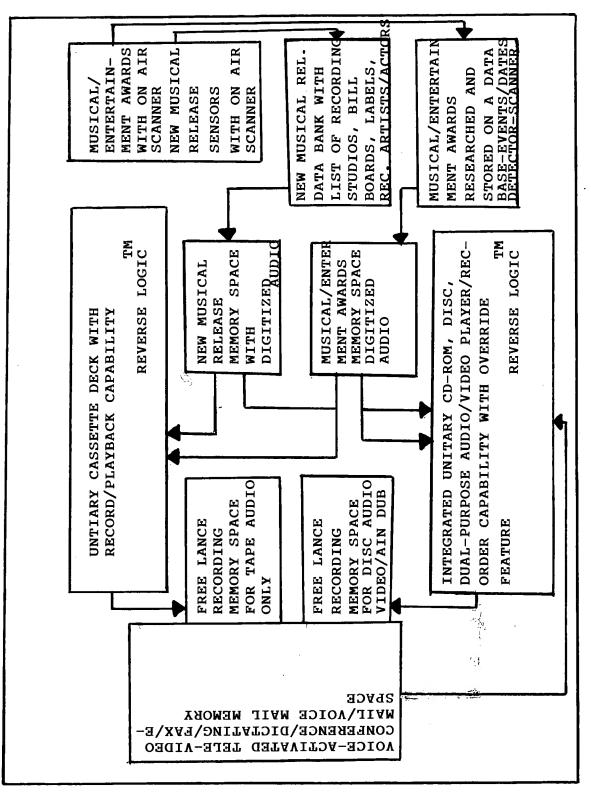


# FIG. 15

# COMPUTER CONTROL COMMAND WITH COMMERCIAL FREE-DISTORTION FREE DUBBING



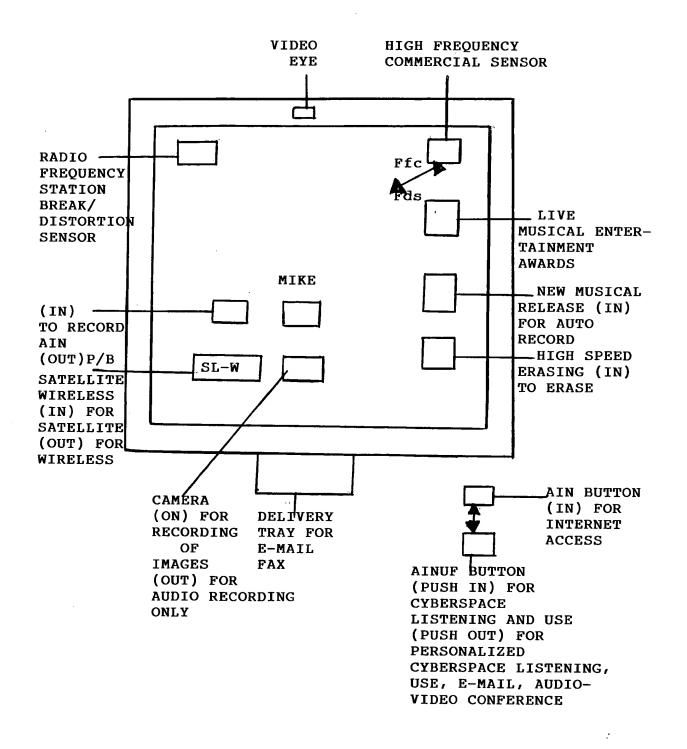
LOGIC MEMORY SPACES- INTERNAL CONFIGURATION AND LOGIC FLOW

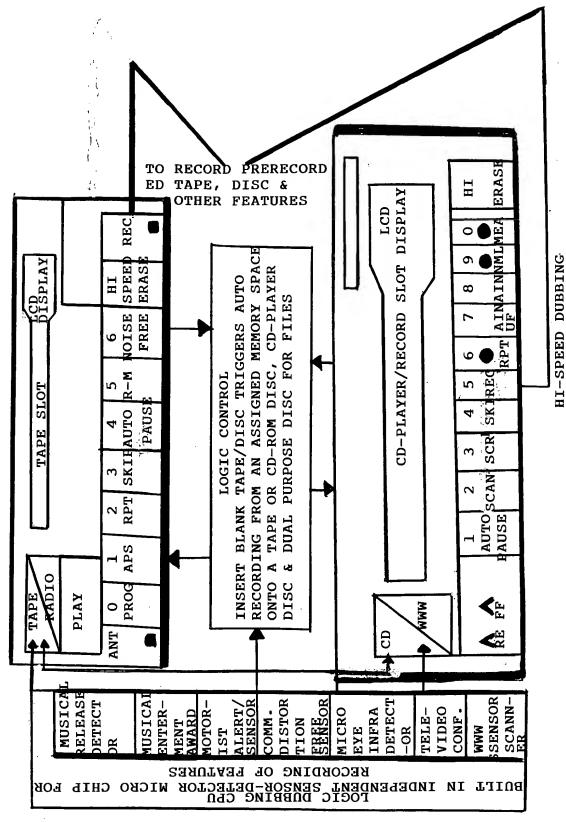




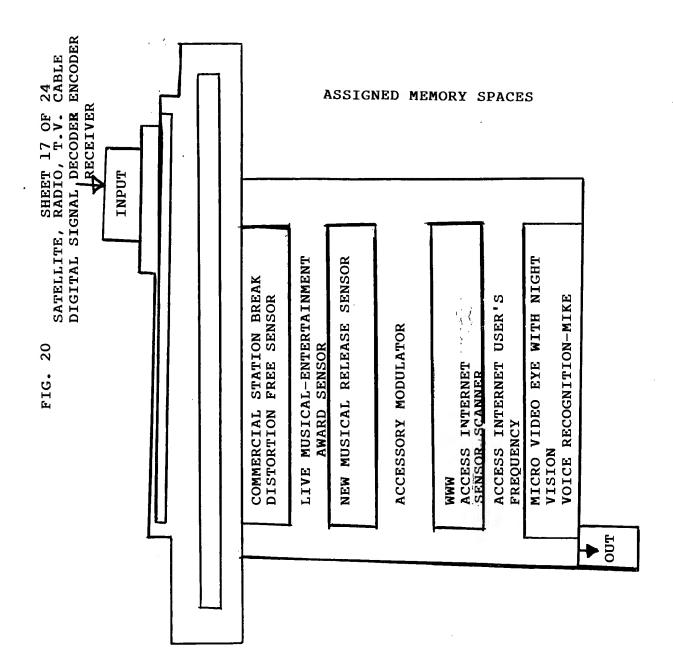


# USE OF FUNCTIONAL PARTS/COMPONENTS











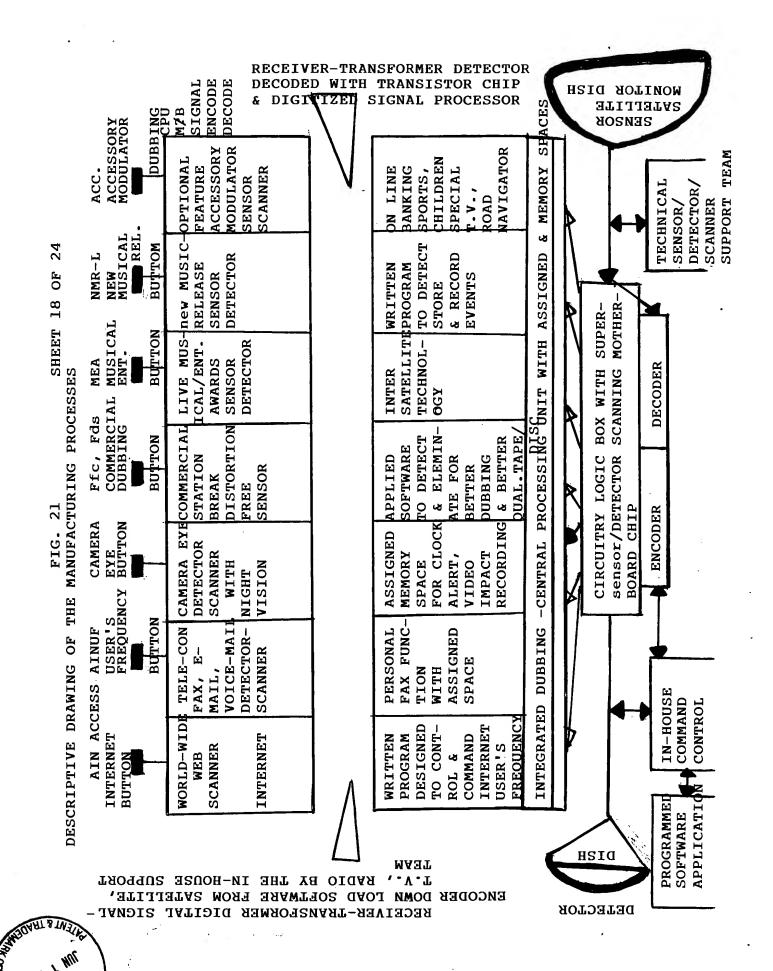
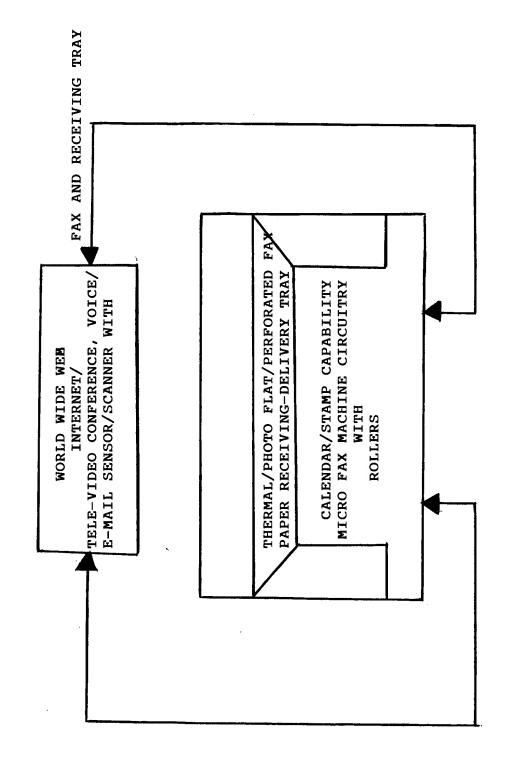


FIG. 22

INTERNET-FAX FUNCTIONS







## REVERSE LOGIC WITH MEMORY SPACES

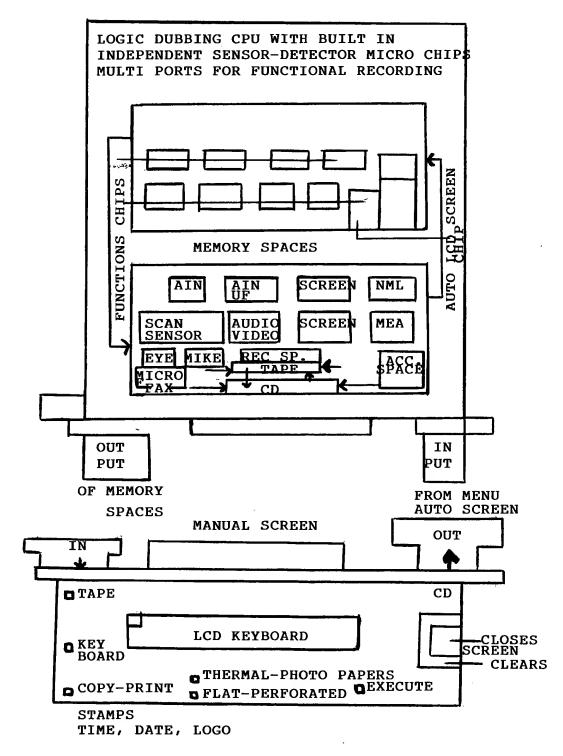
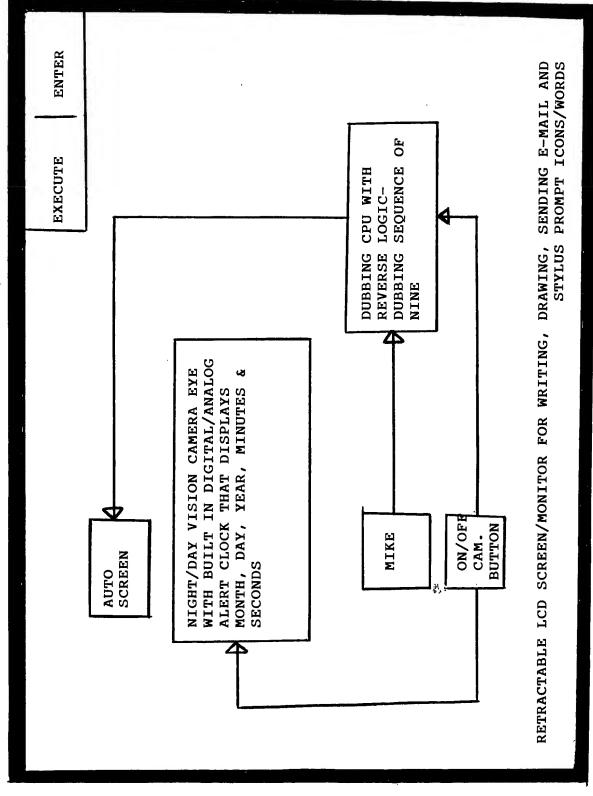
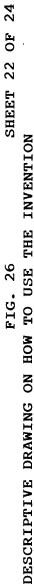


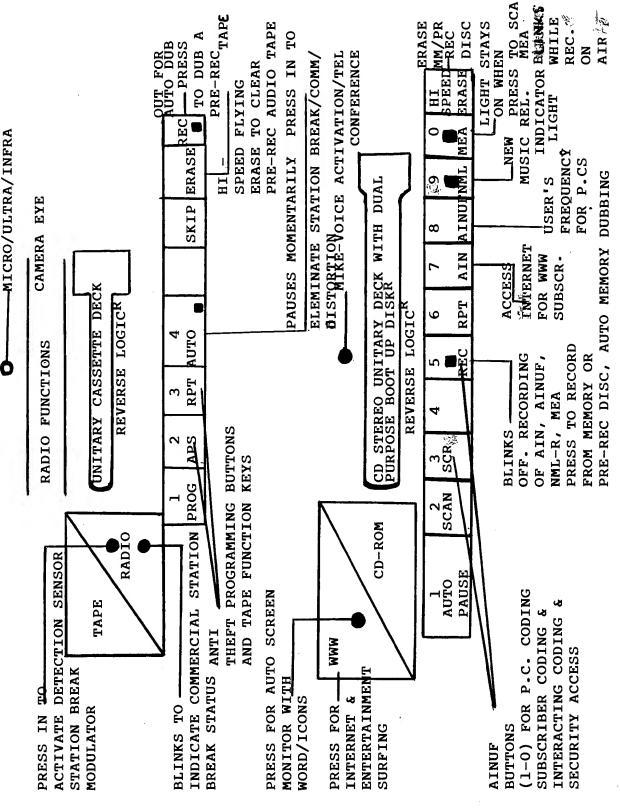
FIG. 24







`







# RETRACTABLE ASSEMBLY HOUSING

